

GLF74130 Ultra-low Power, 4.5A Power Mux Switch with Auto & Manual Input Selection

#### **Product Brief**

# DESCRIPTION

The GLF74130  $I_Q$ Smart<sup>TM</sup> is an advanced technology fully integrated power path load switch with the ability to automatically select between two input sources depending on the input voltage level of each source. The power path switch is targeted for the data storage and mobile markets and is therefore available as a chip scale package utilizing 12 bumps in a 1.27 mm x 1.67 mm x 0.55 mm die size to deliver the highest performance lowest cost power path switch solution in the industry.

The GLF74130 has a built-in reverse current blocking protection. When both switches are at the off mode, the GLF74130 prevents the reverse current from a higher output voltage to the input side.

The EN pin can be used along with the SEL pin to control the switches of the GLF74130. By the combination of these two pins, one of input source selection modes is set among the automatic, VIN1, or VIN2 selection.

#### FEATURES

- Two-Input and Single-Output Power Multiplexer Switch
- Automatic and Manual Input Selection Modes
- Supply Voltage Range : 1.5 V to 5.5 V
- $R_{ON} = 20 \text{ m}\Omega$  Typ. at 5.5  $V_{IN1}$  or  $V_{IN2}$
- 4.5 A Continuous Output Current Capability Per Channel
- Ultra-Low Supply Current at Operation
  I<sub>Q</sub>: 4 uA Typ at 5.5 V<sub>IN</sub>
- Ultra-Low Stand-by Current
  I<sub>SD</sub>: 50 nA Typ at 5.5 V<sub>IN</sub>
- Reverse Current Blocking when Disabled
- Ambient Operating Temperature Range: -40 to 85 °C
- HBM: 6 kV, CDM: 2 kV
- 1.27 mm x 1.67 mm x 0.55 mm, 12 Bump Wafer Level Chip Scale Package

## APPLICATIONS

- IoT Tracking System
- Smart Devices
- Communication / Network System
- Subsystem with Backup Power

## PACKAGE

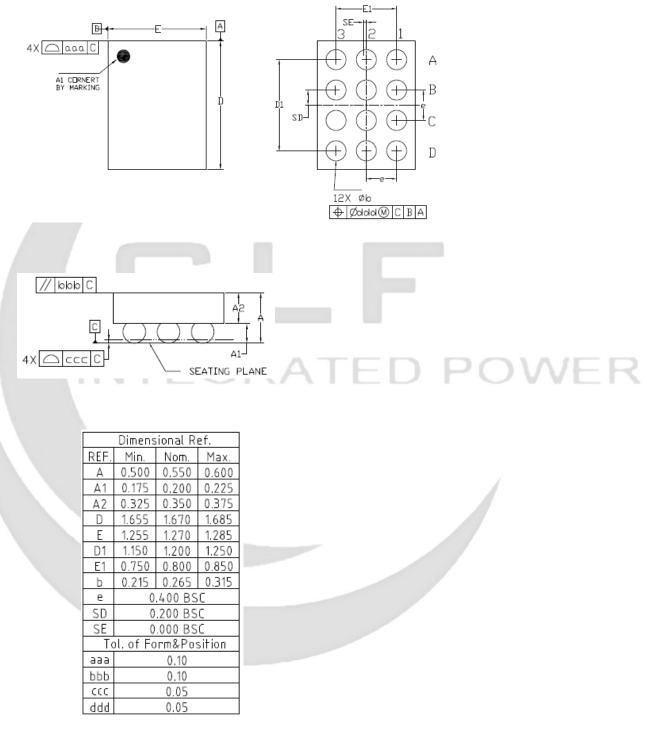
VIN1	VIN1	VIN1		VIN1	VIN1	VIN1			
(A1)	(A2)	(A3)		(A3)	(A2)	(A1)			
VOUT	VOUT	VOUT		VOUT	VOUT	VOUT			
(B1)	(B2)	(B3)		(B3)	(B2)	(B1)			
VIN2	VIN2	VIN2		VIN2	VIN2	VIN2			
(C1)	(C2)	(C3)		(C3)	(C2)	(C1)			
SEL	GND	EN		EN	GND	SEL			
(D1)	(D2)	(D3)		(D3)	(D2)	(D1)			
TOP VIEW				BOTTOM VIEW					

1.27 mm x 1.67 mm x 0.55 mm, 0.4 mm pitch

# PACKAGE OUTLINE

INTEGRATED POWER

**Fi I** 



Notes

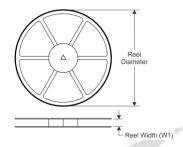
1. AU DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).

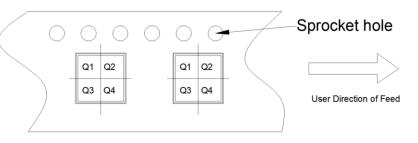
2. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M-1994.

## TAPE AND REEL INFORMATION

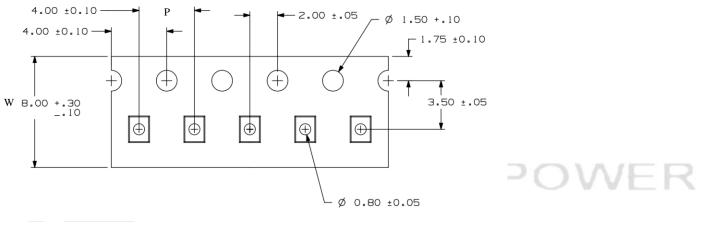
#### REEL DIMENSIONS

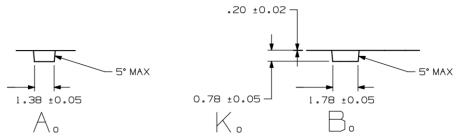






#### TAPE DIMENSIONS





	Device	Package	Pins	SPQ	Reel Diameter(mm)	Reel Width W1	AO	В0	К0	Ρ	w	Pin1
[	GLF74130	WLCSP	12	3000	180	9	1.38	1.78	0.78	4	8	Q1

#### Remark:

- A0: Dimension designed to accommodate the component width
- B0: Dimension designed to accommodate the component length
- C0: Dimension designed to accommodate the component thickness
- W: Overall width of the carrier tape
- P: Pitch between successive cavity centers